



CYGNUS 803

2 Mbps Converter

FEATURES

- Allows devices such as routers to connect to E1 links telecom networks through E1 interface
- CYGNUS 803 supports connection to both framed and unframed E1 links
- E1 interface complies with ITU-T Recommendations G.703 and G.704
- Frame types selectable from: PCM 30 and PCM 31, with and without CRC-4
- Menu driven supervisory software (accessible from a RS232 compatible console port) for configuration, monitoring and statistics reporting
- Built-in pattern generator and checker for qualitative and quantitative error performance monitoring of the E1 interface
- Front panel indicators for displaying unit status
- Can connect multiple DTEs to a single E1 link by cascading a number of CYGNUS 803 units.
- Ordering time options for both CYGNUS 803 and CYGNUS 803L:
 - Operating power supply: 230 VAC, -48 VDC or both
 - E1 interface: 120 ohm balanced (standard), 75 ohm unbalanced or both
 - LCD/keypad for selecting operating parameters

APPLICATIONS

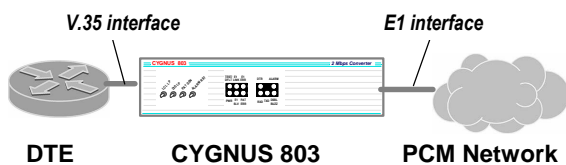


Fig. 1 CYGNUS 803 as a simple interface converter

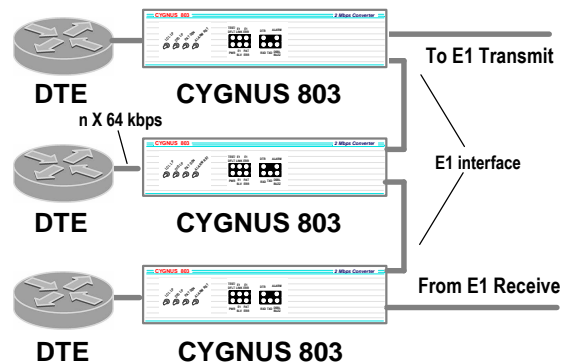


Fig. 2 Multiple units cascaded to allow several DTEs to access a single framed E1 interface

SPECIFICATIONS

E1 PORT

Conformance: ITU-T Rec. G.703, G.704, ITU-T G.823, HDB3 encoding
 Speed: 2048 kbps
 Connector: Balanced 120 ohm on RJ-45 connector; Option: Unbalanced 75 ohm on BNC connector in lieu of, or in addition to balanced interface
 Frame format: - Unframed
 - Framed, selectable as PCM 31 - FAS only, PCM 31C - FAS and CRC-4, PCM 30 - FAS and CAS or PCM 30C - FAS, CAS and CRC-4
 Protection: GD tube in shunt, current limiting device in series

DATA PORT

Conformance: ITU-T V.35.
 Format: Synchronous
 Speed: 2048 kbps or Nx64 kbps
 Data inversion: Selectable
 Connector: 34-pin female

SUPERVISORY PORT

Conformance: ITU-T V.24/RS232
 Format: Asynchronous; 8 data bits, 1 stop bit, no parity
 Speed: Selectable from 4.8, 9.6, 19.2, 38.4 or 57.6 kbps
 Connector: 9 pin 'D' female connector

CLOCKING OPTIONS

E1 Slave clock: The master clock of the communication system is recovered from the signal received by the CYGNUS 803 unit on its E1 port
 DTE Slave clock: The master clock of the communication system is supplied by the DTE connected to the local CYGNUS 803 unit.
 Internal clock: An internal oscillator in the CYGNUS 803 generates the master clock of the communication system.

DIAGNOSTICS

Local loopback, digital loopback, pattern generation and checking

FRONT PANEL INDICATORS

PWR:	Power ON	E1 SLV:	Unit is in E1 Slave mode
TST/DFLT:	A test is on, or unit is operating with default parameters	PAT ERR:	Pattern mismatch
ALARM:	Alarm condition on E1 port	TXD:	Data received from DTE
E1 LINK:	E1 link established	RXD:	Data sent to DTE
E1 ERR:	Error on E1 link	DTR:	DTE is ready
		DSBL:	Audible buzzer (if installed) is disabled
		BUZZ:	

BUZZER (Optional feature)

Resettable buzzer which sounds on failure of E1 link. Provision to shut off or disable the buzzer through a front panel switch.

LCD-KEYPAD (Optional feature)

16 characters x 2 lines, with backlight; Four keys (Next, Back, Select, Quit)

GENERAL

Size: 220 mm (w) x 54 mm (h) x 212 mm (d)
 Power: 230 VAC or -48 VDC, or both: ordering time option

ORDERING INFORMATION

Order Code: CYGNUS 803 W/X/Y/Z

W: E1 interface: Blank for 120 ohm interface; "E1-75" if both 75 and 120 ohm required
 X: LCD/keypad: "LCD" if required, blank if not required
 Y: Input power supply: Blank for AC power supply, otherwise "DC" or "AC&DC"
 Z: Buzzer: "BZR" if buzzer is required, else leave blank

Note: Specifications are subject to change without notice

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